

REMARKS

This response addresses the new Section 112 rejections. This response hereby incorporates the last response and the appeal brief as its response to the remaining prior art rejections. The hope is that the Section 112 rejections can be overcome so that the Board is not unnecessarily burdened with these issues on appeal.

With respect to the Section 112 rejection contained in paragraph 2 concerning claim 1, support may be found in the specification at page 4, lines 1-4 and page 5, lines 8-19.

With respect to the rejection in the same paragraph of claim 11, support may be found at page 8, lines 23-25. The same material supports claim 21.

With respect to the argument on page 3 that the language in the specification talks about communication between processors, rather than transceivers, it should be noted that page 5, lines 1-4 and the figures make it clear that all communications between the processors is done through the optical interfaces 16, which would constitute the claimed transceivers as clearly shown in Figure 2.

With respect to the rejection set forth in paragraph 3, to the extent it is understood, it is believed that it is addressed by the comments above.


Reconsideration is respectfully requested.

With respect to the rejection set forth in paragraph 5 concerning the difference between a device and a system, it is believed that this has already been addressed. It is respectfully submitted that the suggestion that one skilled in the art would have trouble understanding what a device or system means should be reconsidered. There is no need to define terms of this ilk in the specification for anyone skilled in the art.

In view of these remarks, reconsideration is respectfully requested.

Respectfully submitted,

Date: December 17, 2007



Timothy N. Trop, Reg. No. 28,994
TROP, PRUNER & HU, P.C.
1616 South Voss Road, Suite 750
Houston, TX 77057-2631
713/468-8880 [Phone]
713/468-8883 [Fax]

Attorneys for Intel Corporation